

***Administrative Requirement***

A double patenting administrative requirement is not being required by Examiner in the instant application since Examiner has independently conducted a double patenting analysis of the claims in the instant application.

***Information Disclosure Statement***

1. The information disclosure statements (IDS) submitted on 5/5/03, 3/14/03, 6/18/02, 3/19/02, 5/11/01, 4/7/97, 4/17/96, 2/6/96, 12/22/95, 12/11/95, and 12/6/95 are in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statements have been considered by the examiner.

It is noted that for each foreign document and NPL document, listed on the respective PTO-1449 forms filed in the instant application, with no date information a “no date” annotation has been assigned by the examiner to each as the date information was not readily obtainable.

**EXAMINER'S AMENDMENT**

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in an email communication from Carl L. Benson (Reg. No. 38,378) on 9/21/10.

The application has been amended as follows:

In the claims:

1-56. (Cancelled)

57. (Previously Presented) A method of enabling a station of a particular kind to deliver complete programming, said station including a storage device, and said method comprising the steps of:

storing programming at said storage device, said programming comprising a computer program and a portion to be completed by accessing prestored data at said station of a particular kind,

wherein said computer program is operative to complete said portion when executed at said station of a particular kind, said execution of said computer program enabling a processor at said station of a particular kind to select a specific datum from said prestored data and place information, which results from a processing of said selected datum, into said portion to be completed, thereby completing said programming; and

storing a control signal, which is operative at at least one particular kind of station, said control signal operative to cause said execution of said computer program,

whereby said station of a particular kind is enabled to deliver complete programming.

58. (Previously Presented) The method of claim 57, wherein said prestored data designates programming transmitter data, said method further comprising the step of

receiving and storing programming transmitter data.

59. (Previously Presented) The method of claim 57, wherein said prestored data designates subscriber data, said method further comprising the step of storing subscriber data.

60. (Previously Presented) The method of claim 57, wherein said control signal comprises a series or stream of sequentially transmitted control instructions, said method further comprising the step of

storing in said control signal two or more control instructions in a specific order with information designating a time period.

61. (Previously Presented) The method of claim 60, wherein said series or stream of sequentially transmitted control instructions is to be included in a message stream, said method further comprising the step of

storing an instruction which is effective to instruct said processor to process at least one message of said message stream.

62. (Previously Presented) The method of claim 57, wherein said portion to be completed comprises generally applicable information.

63. (Previously Presented) The method of claim 62, wherein said generally applicable information is to be included in machine language code.

64. (Previously Presented) The method of claim 62, wherein said generally applicable information includes higher language code and said computer program operates to generate a module including said higher language code.

65-67. (Cancelled)

68. (Previously Presented) The method of claim 57, wherein a control signal causes a controller operatively connected to said storage station to control a peripheral device, said method further comprising the step of  
storing said control signal.

69-72. (Cancelled)

73. (Previously Presented) The method of claim 57, wherein said storage station is an intermediate transmitter station, said method further comprising the step of transmitting said first programming.

74. (Previously Presented) The method of claim 57, wherein said storage device is an ultimate receiver station.

82-93. (Cancelled)

***Allowable Subject Matter***

3. Claims **57-64, 68, 73, and 74** (*renumbered 1-11, respectively*) are allowed.
4. The following is an examiner's statement of reasons for allowance:

Regarding claim **57**, the closest prior art of record, *Yanagimachi et al. (U.S. 3,936,595)* (*hereinafter Yanagimachi*), teaches a method for controlling the communication of programming signals at a receiver station, where a programming stream is received at a receiver station of Figure 14 that contains a plurality of control codes as spoken of on column 16, lines 22-40.

*Yanagimachi* as well as the other prior art of record fail to teach:

*"storing programming at said storage device, said programming comprising a computer program and a portion to be completed by accessing prestored data at said station of a particular kind,*

*Wherein said computer program is operative to complete said portion when executed at said station of a particular kind, said execution of said computer program enabling a processor at said station of a particular kind to select a specific datum from said prestored data and place information, which results from a processing of said*

*selected datum, into said portion to be completed, thereby completing said programming; and*

*storing a control signal, which is operative at at least one particular kind of station, said control signal operative to cause said execution of said computer program"*  
in combination with the other limitations of claim 57.

Claims **58-64, 68, 73, and 74** are dependent upon claim **57** and are thus also allowable over the prior art of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to MICHAEL J. MOORE, JR., whose telephone number is (571)272-3168. The examiner can normally be reached on Monday-Friday (7:30am - 4:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, William Korzuch can be reached on (571) 272-7589. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Michael J. Moore, Jr./  
Primary Examiner, Art Unit 2467